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In re Application of:	GRISHAW, JAMES EVERETT et al.
Application No.:	10688446
Filing Date:	10/17/03
Title:	BACKWARD-COMPATIBLE PARALLEL DDR BUS FOR USE IN
	HOST-DAUGHTERCARD INTERFACE
Direct to:	U.S. Patent and Trademark Office Mail Stop: Duplicates Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450
N	OTICE UNDER 37 CFR 1.251 – Pending Application
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☐Applicant does not possess application.	any record of the correspondence between the Office and the applicant for the above-identified
Δρril 14, 2005 Date	Signature
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PTO Doc Code: R251.RES (Rev. 03/05)



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